Annotation Zhuravlev V., 2120

CPU operation, Computer in use, 2010

The text deals with structure of most CPU’s operations, representation of programs and their storage in computer memory. Three steps operations Von Neumann CPU’s are considered. There are fetch, decode and execute.

In addition, program counter for location in memory, CPU’s Instruction Set Architecture (ISA), CPU registers and their purpose are described. Further on, the “flags” register, their function and use are studied. In conclusion, execute commands in more complex CPU than the one described in this text are mentioned.

This information may be of interest to students, IT specialists and research community engaged in studying the execute programs in CPU’s.